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Practitioner's Docket No. MI22-1572

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Filed: 01/09/01

Salman Akram

Group No.: 2827

Examiner: David A. Zarneke

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For: Methods of Forming Board-On-Chip Packages

Commissioner for Patents Washington, D.C. 20231

Application No.: 09/756,971

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

Facsimile Transmittal Supplemental Information Disclosure Statement PTO-Form 1449 and 2 cited references

Robin Saldivia

Total Pages 23

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(Certification of Facsimile Transmission-page 1 of 1)

Application Serial No 09/756,971
Filing Date January 9, 2001
Inventor
Assignee Micron Technology, Inc.
Group Art Unit
Examiner David A. Zarneke
Attorney's Docket No
Title: Methods of Forming Board-On-Chip Packages

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449. No admission is made regarding whether the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 7-/2-04

D. Brent Kenady Reg. No. 40,045

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							FILING DATE January 9, 2001			GROUP 2827		
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*EXAMINER: Initial if reference considered, whether or not eliation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												